

Scratch Testing of Low k Dielectric Films and a Correlation Study of the Results

Introduction

Processing that is required to lower the dielectric constant of a low k film can have the adverse effect of degrading the mechanical properties of the film. Low k films are subjected to many processes that test the strength of these films and their adhesion to the substrate, such as chemical-mechanical polishing (CMP) and wire bonding. It is important for these materials to resist plastic deformation during these processes and remain intact without blistering up from the substrate. Ideally, a dielectric material will have a high hardness and elastic modulus, because traditionally, these two parameters help to define how the material will react when subjected to manufacturing processes. In this application note, scratch tests are performed on several low k samples using a rampload scratch test. The results from scratch testing and nanoindentation are examined through correlation analysis to better understand the interconnectedness of scratch results.

Samples

Ten low k samples were provided for scratch testing by SBA Materials, Inc. The films were deposited on silicon substrates by spin coating and their thicknesses varied between samples from 375nm to 743nm. Samples were supplied with the elastic modulus and hardness measurements of the films. Mechanical properties data were collected using a Nano Indenter® G200 instrument using a special test method for measuring the substrate-independent properties of low k dielectric films. This test method is described elsewhere.¹

Test Methodology

The scratch tests were performed on the Nano Indenter G200 system, which is powered by electromagnetic actuation to achieve a high dynamic range in force and displacement. The instrument's design avoids lateral displacement artifacts during the scratch process. Using the Nano Indenter G200 system, researchers can measure Young's modulus and hardness in compliance with ISO 14577, in addition to scratch and wear properties. Deformation can be measured over six orders of magnitude (from nanometers to millimeters). A ramp-load scratch test was used to conduct three tests on



Step 1: Original Surface Scan



Step 2: Ramp Load Scratch Segment



Step 3: Residual Deformation Scan

Figure 1. Diagram of the three-step ramp-load scratch test. Red lines show the areas of pre- and post- profile scans used to perform leveling of the three steps.

each wafer in three locations—a total of nine tests for each sample. In a ramp-load scratch test, a tip is brought into contact with the sample; then, the tip is loaded at a constant loading rate while the sample is simultaneously translated. Prior to and following the scratch test, a single-line scan of the surface topography is completed for comparing the original surface to the deformation caused by the scratch test. Therefore, each scratch test consists of three steps: a single-line pre-scan of the area to be scratched, the ramp-load scratch test, and a final scan to evaluate the residual deformation. Before and after each step, a pre-scan and a post-scan, usually equal to 20% of the scratch length, is performed so that the software can automatically align the data in the three steps. The original and residual single-line scans allow for the evaluation of deformation mechanisms and the quantification of deformation. The scratch process is diagrammed in Figure 1.



Figure 2. Diagram of a cube-corner tip.

When performing scratch testing on any sample set, it is critical that all test parameters and tip geometries remain consistent throughout the samples being compared. This ensures that qualitative comparisons can be made using the resulting data.

The tip chosen for conducting the scratch tests was a cubecorner tip with a tip radius that was nominally less than 20nm. A cube-corner tip creates a triangular projected contact with the sample; this tip geometry creates high levels of stress in the material during the scratch. Scratches can be performed either face-forward or edge-forward when using a pyramid-shaped indenter. Scratching face-forward with the cube-corner tip acts like a snow plow and pushes the material out of the way, while edge-forward cuts the material like a knife. A diagram of a cube-corner tip is shown in Figure 2. The low k samples were tested using the cube-corner tip positioned so that it scratched face-forward.

Results and Discussion

Film Failure

All of the low k films failed in a similar manner; these films exhibited plastic deformation up to a critical point where blistering of the film occurred. Following blistering, complete failure of the film occurred and the substrate was scratched for the remainder of the test. For complete understanding of the mode of failure exhibited during the scratch tests on the low k samples, a typical scratch test was analyzed using scanning probe microscopy, which is available on the Nano Indenter G200 system by using the NanoVision option. The NanoVision option allows imaging using a high-precision piezo translation stage; lateral resolution and flatness of travel are better than 2nm. This system allows quantitative imaging and high precision targeting for the investigation of material properties.

The scratch test that was used for the imaging analysis took the film to failure and the displacement results are shown in Figure 3. The Critical Load was chosen automatically by the software through examination of the residual displacement curve. In the calculation of the critical load point, the NanoSuite software examines the rate of change of the displacement in the residual scan (the orange trace in Figure 3). Then, using these data, the software extrapolates the displacement curve for the next data point based on the change in the scratch distance. The actual observed displacement is compared to the predicted displacement and the magnitude of the differential is recorded as the residual roughness Level. Figure 4 shows a plot of the residual roughness along with the residual deformation of the scratch; for presentation, the residual roughness channel has been multiplied by three so that it will appear on a graph with the displacement curves. The critical load was placed in the location where the residual roughness was greater than 3nmhence, this is the location where the anticipated displacement is in error (as compared to the actual displacement) by greater than 3nm. The tolerance for placing the critical load is determined by the roughness detected during the surface scan of the pre-profile.



Figure 3. The displacement into surface versus scratch distance data for the scratch that was used for imaging to further examine failure; the blue trace is the original surface topography, the green trace is the scratch curve, and the orange trace is the residual deformation. The locations of blistering and total film failure are labeled.

To examine the blistering and failure of the low k films, the scratch test performed in Figure 3 was imaged with NanoVision. Figure 5 shows the top and side views of the scratch test that was performed in Figure 3. Here it is clear that a large amount of material has blistered just prior to failure of the film. The scale shows that the film blistered up approximately 400nm above the film surface. Typically, blistering is not measured well by the residual scan of a scratch test because the film supports very little load parallel to the scratch direction and is usually pushed down by the profiler. However, NanoVision was used to scan the scratch deformation in the transverse direction which allowed the detailed observation of the blister. To ensure that blistering was being observed as opposed to pile-up, another scan at the start of blistering was conducted. Figure 6 shows the scan of the scratch test at the start of blistering. It is evident from the scan that the residual scratch depth increases until blistering occurs; then, the residual deformation in the film is lifted upward off of the substrate due to blistering. If pile-up had occurred, the



Figure 4. Residual roughness (blue) and the residual deformation scan (orange) versus scratch distance.



Figure 5. Typical blistering and failure in the low k films. Scratch imaging was performed using the NanoVision option. Notice that the film has blistered up approximately 400nm from the surface.



Figure 6. Typical scratch tests in the low k films showing the start of blistering. Notice that the residual scratch depth increases until blistering occurs. Following the start of blistering, the residual deformation in the film has been lifted upwards.

residual scratch depth would have continued to increase as the scratch test progressed.

Scratch Results

A summary of the scratch results for all ten samples is provided in Table 3. The sixth column of this table provides the amount of elastic deformation that occurred during the scratch up to the point of film failure. Elastic and plastic deformation during the scratch test is determined by measuring the areas between the scratch curve and the original surface topography, and between the residual deformation scan and the original surface topography. This new parameter, percent elastic deformation up to critical load, provides a measure of the film's resistance to permanent deformation and provides a more complete evaluation of the film's performance by quantifying not only the load at which film failure occurs, but also the type of deformation occurring up to film failure. The areas of elastic and plastic deformation are shaded on the scratch curves of Figure 7.



Figure 7. Elastic and plastic deformation of a scratch test performed on Sample J; the elastic deformation is shaded in blue and the plastic deformation is shaded in yellow. Sample J film thickness is 650nm.

Figures 8 and 9 graphically display the results of critical load and total deformation, and the percent penetration/elastic deformation and elastic modulus for the four top-performing samples based on the results of critical load (B, C, E, and J). Here it is shown that there are subtle differences between the top performers. Samples B and J had the highest critical loads of all the samples and showed no significant statistical difference in the result for critical load. However, Sample J results have a much smaller standard deviation, suggesting that Sample J is more repeatable and predictable.

Sample C possessed the second lowest total deformation and the highest amount of elastic deformation of these top performing samples; this is quite significant since this sample was the thickest sample tested (by approximately 100nm). So, even though Sample C had the most material available for accommodating deformation, it showed the highest resilience of all the samples. In addition, its performance in critical load was on par with the other samples. Similarly, the results for Sample E are significant because the film thickness for this sample is thinner than any of the other top performers by approximately 100nm. While Sample E did not have the highest critical load, it did possess the least amount of total deformation and had one of the highest results for percent penetration at critical load.

After presentation of the results to the manufacturer, it was disclosed that samples E and J had the same chemical composition but used different solvents for dilution during the spin coat process. Therefore, either the difference in film thickness or the use of different solvents caused the two samples to have different critical load values. There is insufficient information to conclude whether the solvent or film thickness is responsible for the lower critical load value in Sample E. Given



Figure 8. Critical load and total deformation for the four top performing samples (based on critical load). Ideally, critical load results would be high and total deformation results would be low.

that the percent penetration in Figure 9 is the same for both samples E and J, the data would suggest that film thickness is the main cause; however, because different solvents were used in application, a final conclusion cannot be drawn for the direct cause of the lower critical load. These two samples did show statistical differences in the results for elastic modulus, confirming that the films have slightly different mechanical properties.



Figure 9. Percent penetration/elastic deformation and elastic modulus for the four top performing samples (based on critical load).

Correlation of Results

It is easy to look at a large matrix of test results, focus on large numerical differences in single columns, and overlook significant independent results. By examining the correlation of results, patterns can be recognized, ensuring that the sample results are analyzed based on independent results instead of on groups of results that have strong correlations to a single parameter. A good example of a strong linear correlation is in Figure 8 where there is obviously a strong correlation between the critical load and total deformation; if a researcher decided to neglect other information and focus on these two parameters as a basis for analyzing the performance of these films, both results would draw the same conclusion. Many times, correlations are not as easily seen as demonstrated in Figure 8—this one is easily recognized because there happens to be a 97% linear correlation between these two results—it is when a correlation drops below 60% that it becomes hard to recognize. For analyzing the correlation of the results in these scratch tests, normal correlation analysis was conducted and is described elsewhere.² In order to rate the levels of correlation between results, four levels were defined: very weak correlation (40 to 50%), weak correlation (50 to 60%), moderate correlation (60 to 75%), and strong correlation (>75%).

It is apparent from the correlation analysis that all of the results—save for hardness and elastic modulus, which were measured using a test method specifically developed for measuring substrate-independent properties—are, at least, weakly dependent on film thickness. The critical load variation, for example, can be accounted for by a linear relationship with film thickness approximately 67% of the time. However, these results should not be neglected based on this correlation, because this is only a moderate correlation, with Sample E showing an obvious exception; Sample E is one of the thinnest samples but also had one of the highest critical loads. Some of the correlations shown in the table are of no surprise, such as percent penetration and total deformation with a correlation of 86%; higher penetration at film failure would logically yield higher total deformations. In fact, if the total deformation is correlated to penetration depth at critical load, as opposed to percent penetration which is normalized by the film thickness, the correlation factor jumps to over 98%. Some of the more surprising results were the almost complete absences of correlations to results of hardness, elastic modulus, and percent elastic deformation. It is speculated that a correlation to hardness is absent because the interface between the film and substrate failed prior to failure of the film itself.

Conclusions

Ramp-load scratch testing was used for evaluating the scratch response of low k films on silicon substrates. All of the low k samples tested experienced blistering of the film well before complete film failure occurred, and imaging (using the NanoVision option) was completed for confirmation of the failure mode. In addition to providing significant statistical differences in the results of the scratch tests, the results themselves were analyzed using correlation analysis. All of the scratch results were found to have at least a very weak correlation to film thickness. This weak dependence on film thickness makes intuitive sense, because in usual scratch tests the film does not fail until the probe has significantly penetrated the film. Hence, the stresses from the scratch test propagate well into the substrate. There were also some surprising results of complete absence of correlation. Neither hardness nor elastic modulus correlated to any of the scratch parameters, and the percent of elastic deformation up to the critical load did not correlate to any result other than film thickness, which was only a very weak correlation.

Sample J was determined to be the best performing sample due to its excellent ability to resist deformation, withstand a high percentage of penetration, and support the highest load before failure. Even though the results on Sample J were very similar to those on Sample B, Sample J was selected because the tests contained a lower standard deviation, demonstrating that it was more repeatable and predictable. We would like to thank SBA Materials for supplying the samples and some of the results for this study.

References

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